

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Soichi Kobayashi et al.

Application No.: 10/632,928

Filing Date: August 4, 2003

Title: SEMICONDUCTOR INTEGRATED
CIRCUIT CAPABLE OF TESTING
WITH SMALL SCALE CIRCUIT
CONFIGURATION



Group Art Unit: 2117

Examiner: SAQIB JAVAID SIDDIQUI

Confirmation No.: 4942

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following extension of time is requested to: respond to the outstanding Office Action dated July 10, 2007 for

Three Months to January 10, 2008

☒ \$ 1050

☐ \$ 525

☐ The shortened statutory period has been reset by an Advisory Action dated _____.

☐ An Extension fee in the amount of _____ is enclosed.

☐ Charge _____ to Deposit Account No. 02-4800.

☒ Charge \$ 1050.00 to credit card. Form PTO-2038 is attached.

The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BUCHANAN INGERSOLL & ROONEY PC

Date: January 9, 2008

By: _____

Jonathan Bowser

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1050.00 OP